

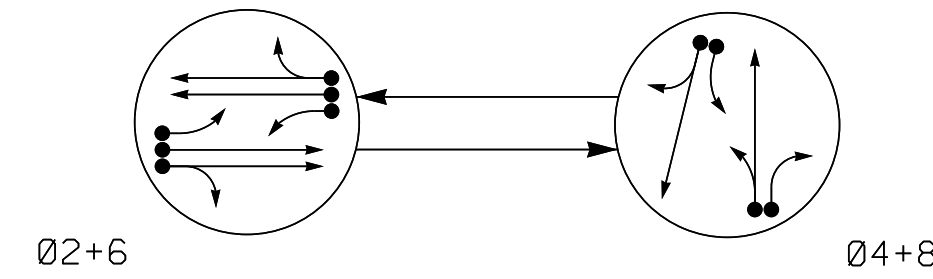
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2 Phase Fully Actuated D04-17 Wilson

PHASING DIAGRAM



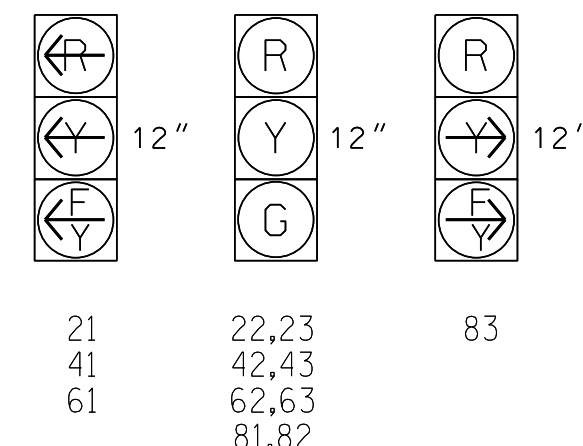
PHASING DIAGRAM DETECTION LEGEND

- ←●→ DETECTED MOVEMENT
- ←○→ UNDETECTED MOVEMENT (OVERLAP)
- ←- - -→ UNSIGNALIZED MOVEMENT
- ←- - -> PEDESTRIAN MOVEMENT

SIGNAL FACE	PHASE		
	02+6	04+8	F
21	F	R	Y
22,23	G	R	Y
41	R	F	R
42,43	R	G	R
61	F	R	Y
62,63	G	R	Y
81,82	R	G	R
83	R	F	R

SIGNAL FACE I.D.

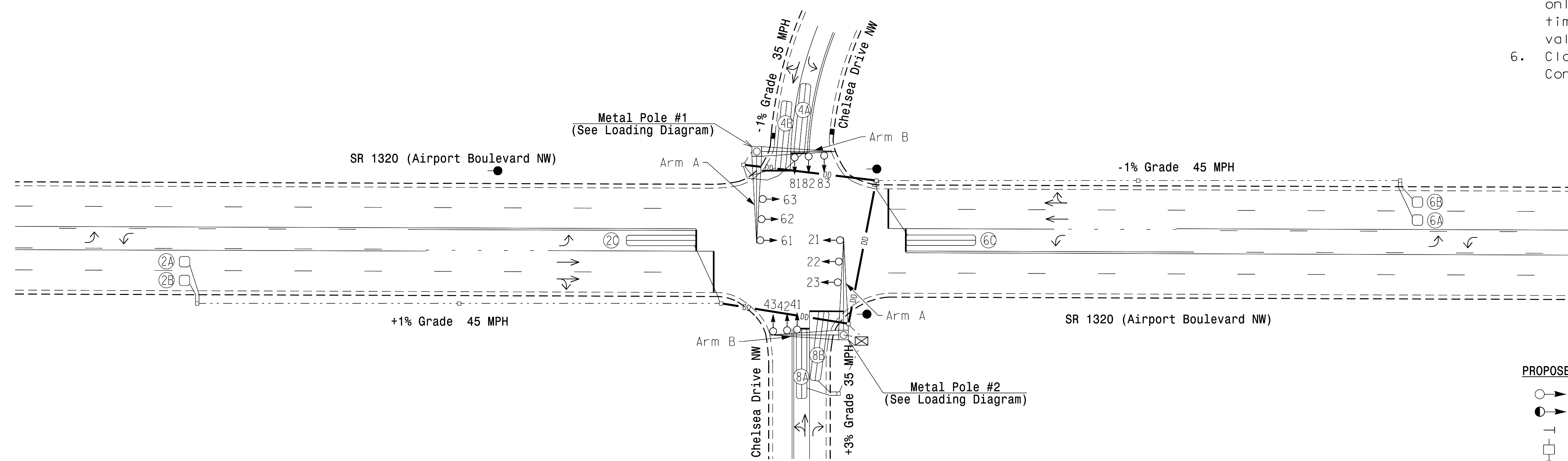
All Heads L.E.D.



MAXTIME DETECTOR INSTALLATION CHART											
DETECTOR						PROGRAMMING					
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND INITIAL	CALL DURING GREEN DELAY	NEW CARD	
2A	6X6	300	5	X	2	-	-	X	X	X	-
2B	6X6	300	5	X	2	-	-	X	X	X	-
2C	6X40	0	2-4-2	X	2	3	-	X	-	X	X
4A	6X40	0	2-4-2	X	4	3	-	X	-	X	X
4B	6X40	0	2-4-2	X	4	10	-	X	-	X	X
6A	6X6	300	5	X	6	-	-	X	X	X	-
6B	6X6	300	5	X	6	-	-	X	X	X	-
6C	6X40	0	2-4-2	X	6	3	-	X	-	X	X
8A	6X40	0	2-4-2	X	8	3	-	X	-	X	X
8B	6X40	0	2-4-2	X	8	15	-	X	-	X	X

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Set all detector units to presence mode.
- Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- Closed loop system data: Controller Asset #: 1457.

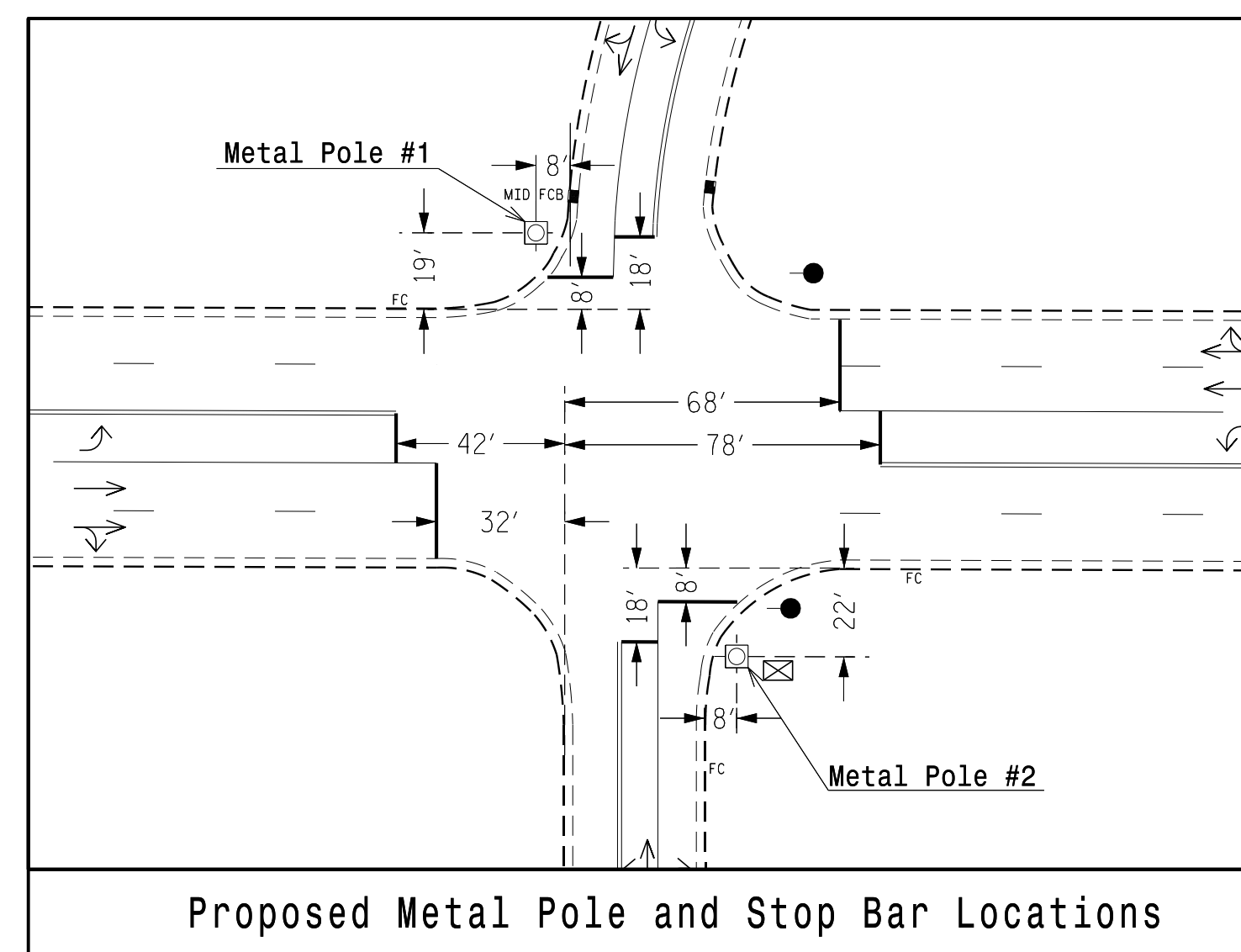


LEGEND

- | PROPOSED | EXISTING |
|----------|----------|
| | |
| | N/A |
| | |
| | |
| | |
| | |
| | |
| | |
| | |
| N/A | |
| | |
| | |
| N/A | |
| N/A | |
| N/A | |

FEATURE	PHASE			
	2	4	6	8
Walk *	-	-	-	-
Ped Clear *	-	-	-	-
Min Green	12	7	12	7
Passage *	6.0	2.0	6.0	2.0
Max 1 *	90	30	90	30
Yellow Change	4.6	3.9	4.6	3.7
Red Clear	1.2	1.4	1.2	1.6
Added Initial *	1.5	-	1.5	-
Maximum Initial *	34	-	34	-
Time Before Reduction *	15	-	15	-
Time To Reduce *	30	-	30	-
Minimum Gap	3.0	-	3.0	-
Advance Walk	-	-	-	-
Non Lock Detector	-	X	-	X
Vehicle Recall	MIN RECALL	-	MIN RECALL	-
Dual Entry	-	X	-	X

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



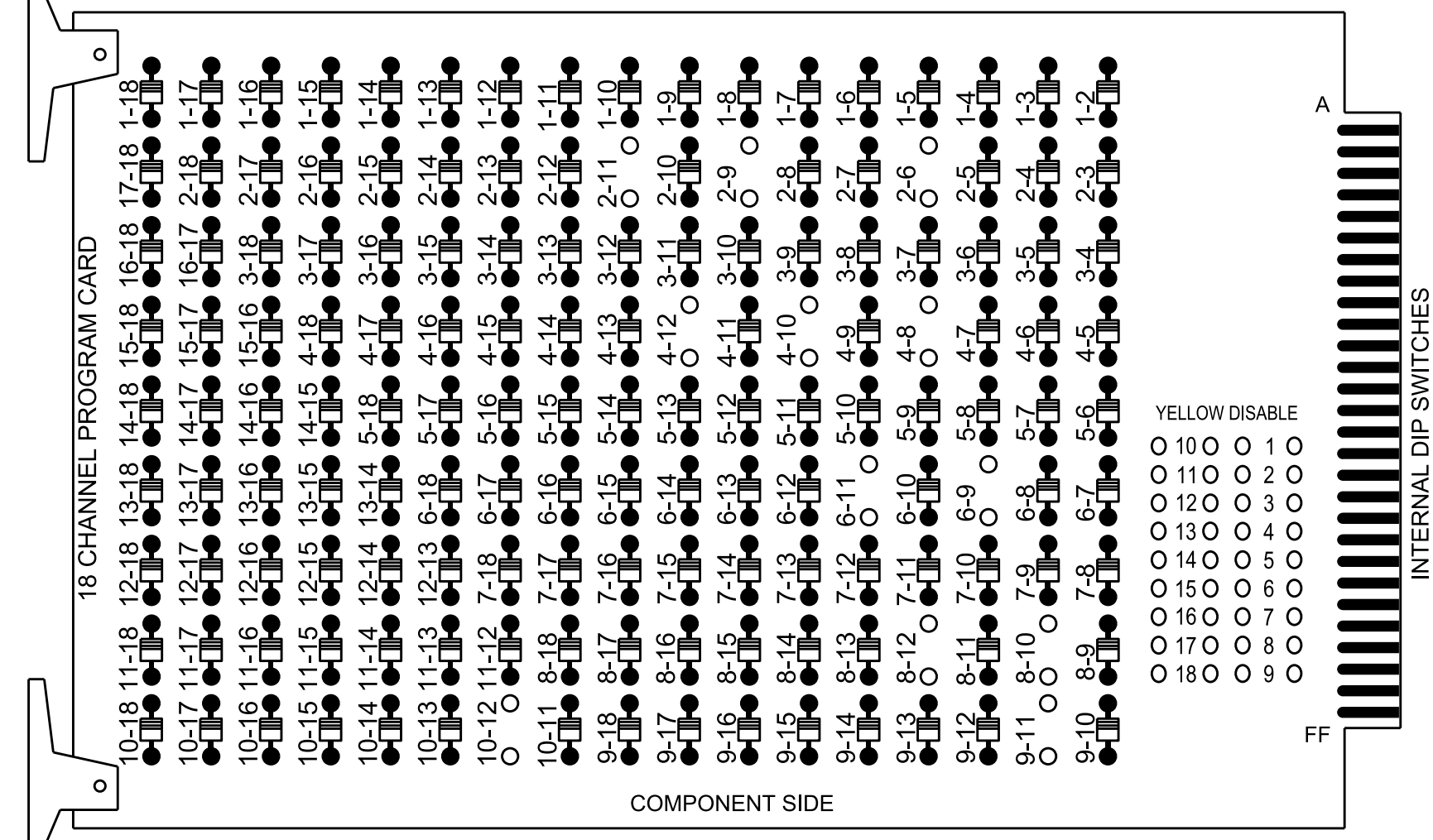
New Installation - Corr. File No. 04-21-63601

	<p>SR 1320 (Airport Boulevard NW) at Chelsea Drive NW</p>		
	<p>Division 4 Wilson County Wilson</p>		
<p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>PLAN DATE: August 2023</p>	<p>REVIEWED BY: ZML</p>	<p>DATE: 09/22/2023</p>
<p>PREPARED BY: KGP, Jr.</p>	<p>REVIEWED BY:</p>	<p>REVISIONS</p>	<p>INIT. DATE</p>
<p>SCALE: 0 40 1" = 40'</p>		<p>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</p>	

18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-6, 2-9, 2-11, 4-8, 4-10, 4-12, 6-9, 6-11, 8-10, 8-12, 9-11, and 10-12.



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program phases 4 and 8 for Dual Entry.
3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
5. The cabinet and controller are part of the D04-17 Wilson System.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....18 With Aux. Output File
 Load Switches Used.....S2, S5, S8, S11, AUX S1, AUX S2, AUX S4, AUX S5
 Phases Used.....2, 4, 6, 8
 Overlap "1".....*
 Overlap "2".....*
 Overlap "3".....*
 Overlap "4".....*

*See overlap programming detail on this sheet.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	22,23	NU	NU	42,43	NU	NU	62,63	NU	NU	81,82	NU	61	83	NU	21	41	NU
RED		128			101			134			107			A124				
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW													A121			A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW																		

*See pictorial of head wiring in detail this sheet.

NU = Not Used

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	∅ 2	∅ 2	T	T	∅ 4	T	T	T	T	T	T	T	FS
L	T	2A	2C	T	T	4A	T	T	T	T	T	T	T	DC ISOLATOR
U	S	∅ 6	∅ 6	T	T	∅ 8	T	T	T	T	T	T	T	S
L	T	6A	6C	T	T	8A	T	T	T	T	T	T	T	ST
U	S	∅ 6	∅ 6	T	T	∅ 8	T	T	T	T	T	T	T	S
L	T	6B	NOT USED	T	T	8B	T	T	T	T	T	T	T	DC ISOLATOR

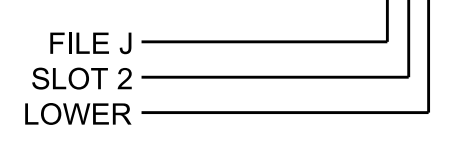
EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

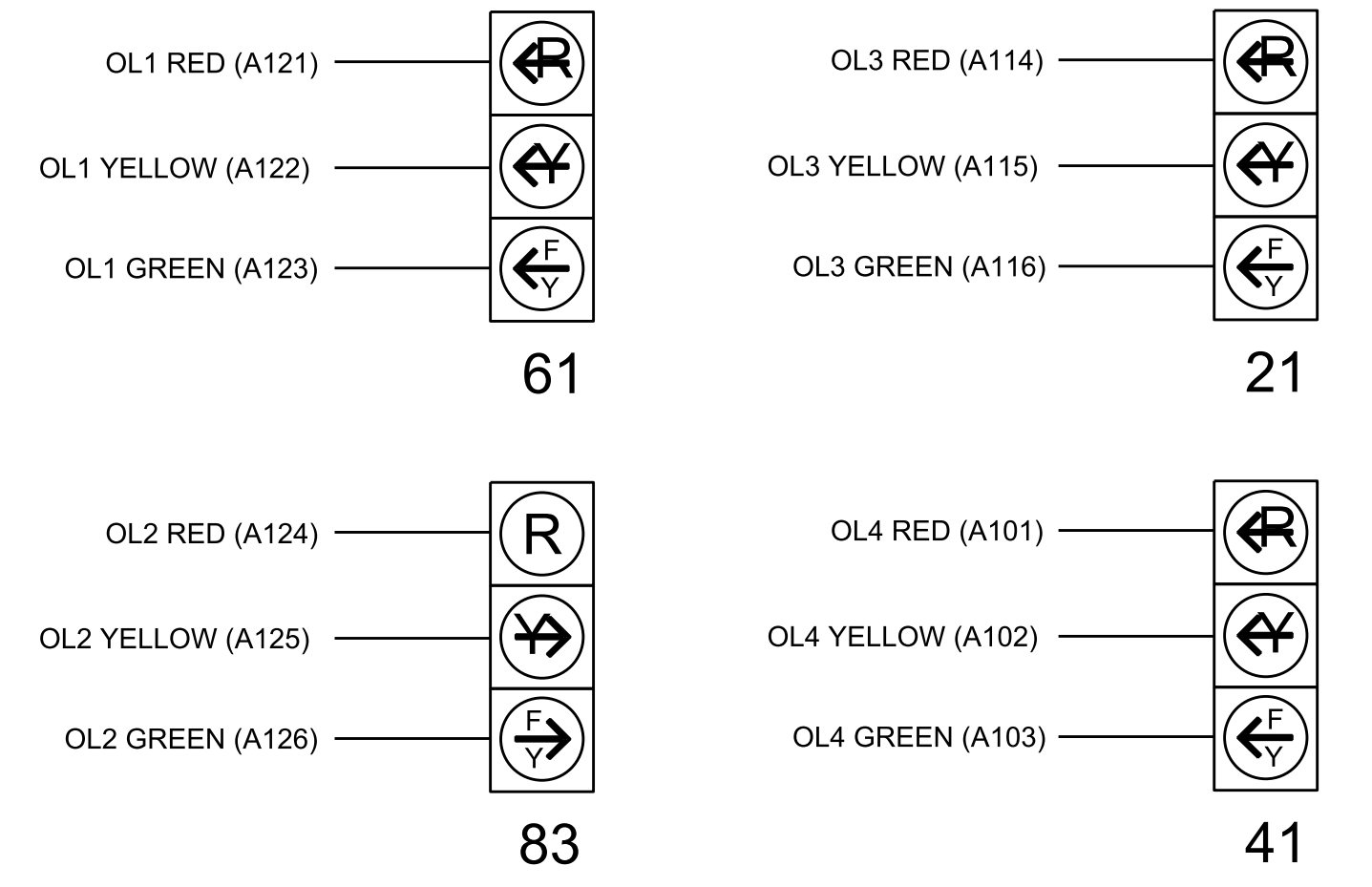
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A	TB2-5,6	I2U	39	1	2	2			X	X	X	
2B	TB2-7,8	I2L	43	5	3	2			X	X	X	
2C	TB2-9,10	I3U	63	29	4	2	3		X		X	X
4A	TB4-9,10	I6U	41	3	8	4	3		X		X	
4B	TB4-11,12	I6L	45	7	9	4	10		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	
6C	TB3-9,10	J3U	64	30	18	6	3		X		X	X
8A	TB5-9,10	J6U	42	4	22	8	3		X		X	
8B	TB5-11,12	J6L	46	8	23	8	15		X		X	

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



OVERLAP PROGRAMMING

Front Panel
Main Menu > Controller > Overlap > Overlap Parameters/Overlap Timings

Web Interface
Home > Controller > Overlap Configuration > Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section	FYA 4 - Section
Included Phases	2	8	6	8
Modifier Phases	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1457
 DESIGNED: August 2023
 SEALED: 9/22/2023
 REVISED:

Electrical Detail

Prepared in the Offices of:

 750 N. Greenfield Pkwy, Garner, NC 27529

Division 4 Wilson County Wilson

SR 1320
(Airport Boulevard NW)
at
Chelsea Drive NW

PLAN DATE: October 2023 REVIEWED BY: DTJ
 PREPARED BY: D.J. Craddock REVIEWED BY:

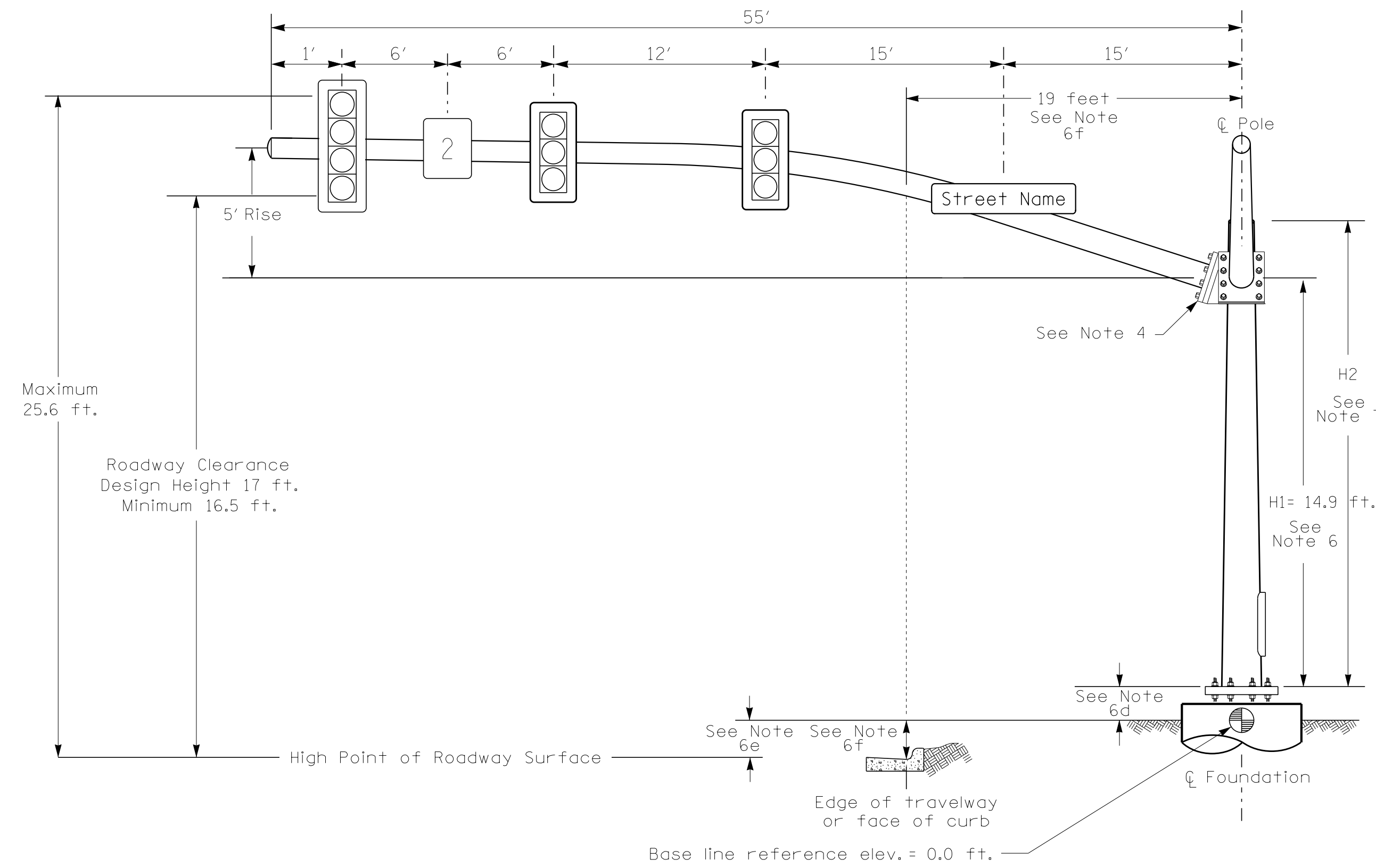
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

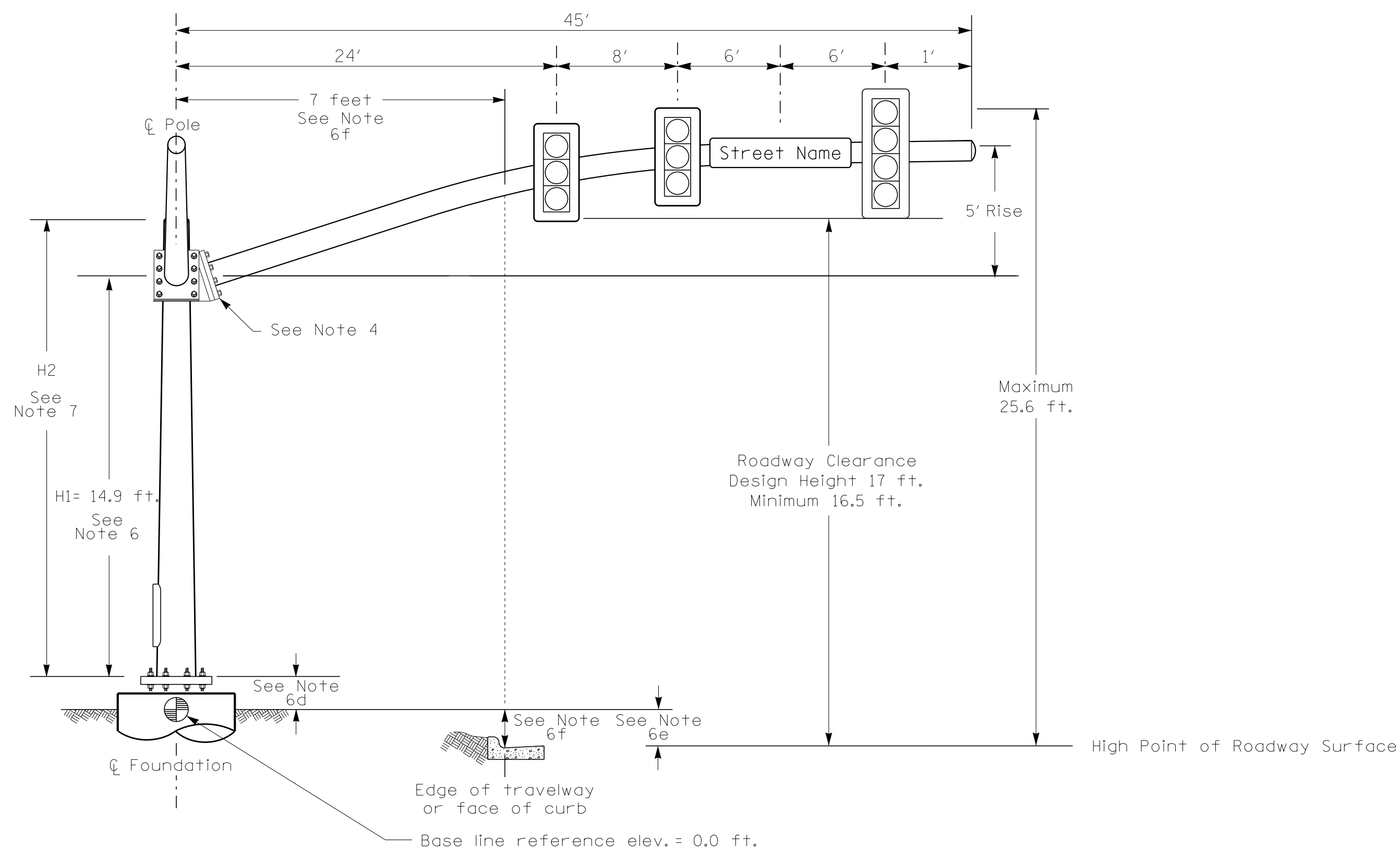
 D. Todd Joyce
 10/09/2023
 SIG. INVENTORY NO. 04-1457

Design Loading for METAL POLE NO. 1, MAST ARM A



Elevation View @ 270°

Design Loading for METAL POLE NO. 1, MAST ARM B



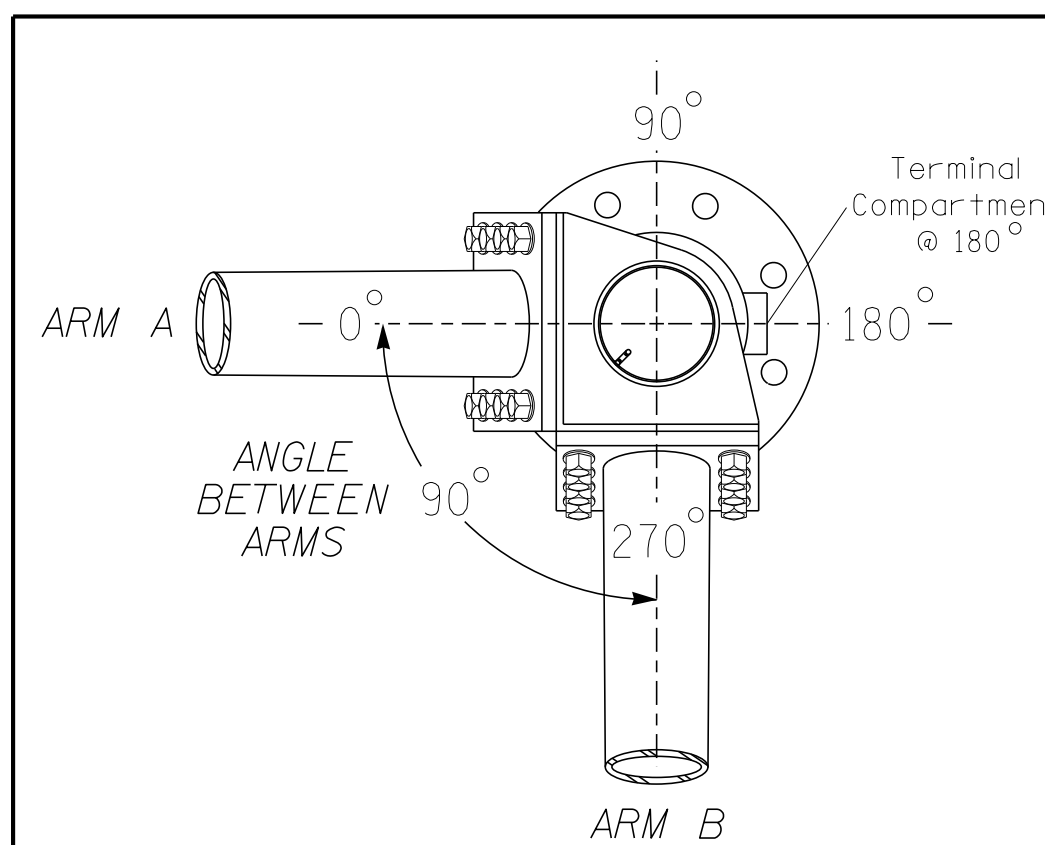
Elevation View @ 0°

SPECIAL NOTE

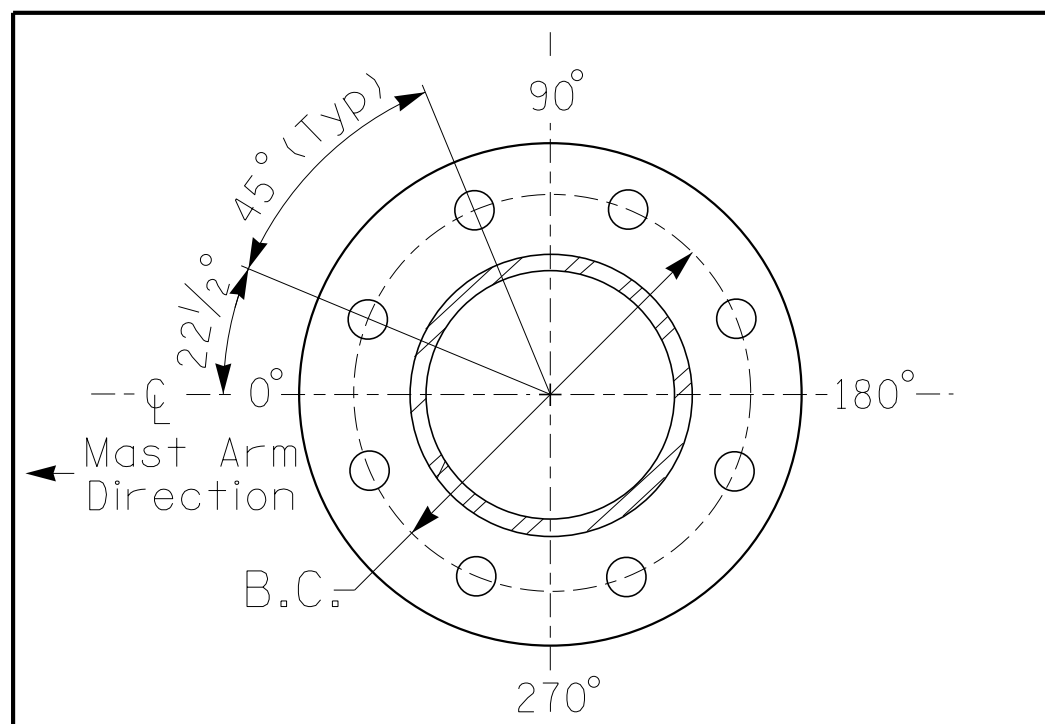
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Arm A	Arm B
Baseline reference point at Foundation @ ground level	0.0 ft.	0.0 ft.
Elevation difference at High point of roadway surface	+0.8 ft.	-0.2 ft.
Elevation difference at Edge of travelway or face of curb	-0.4 ft.	-0.8 ft.

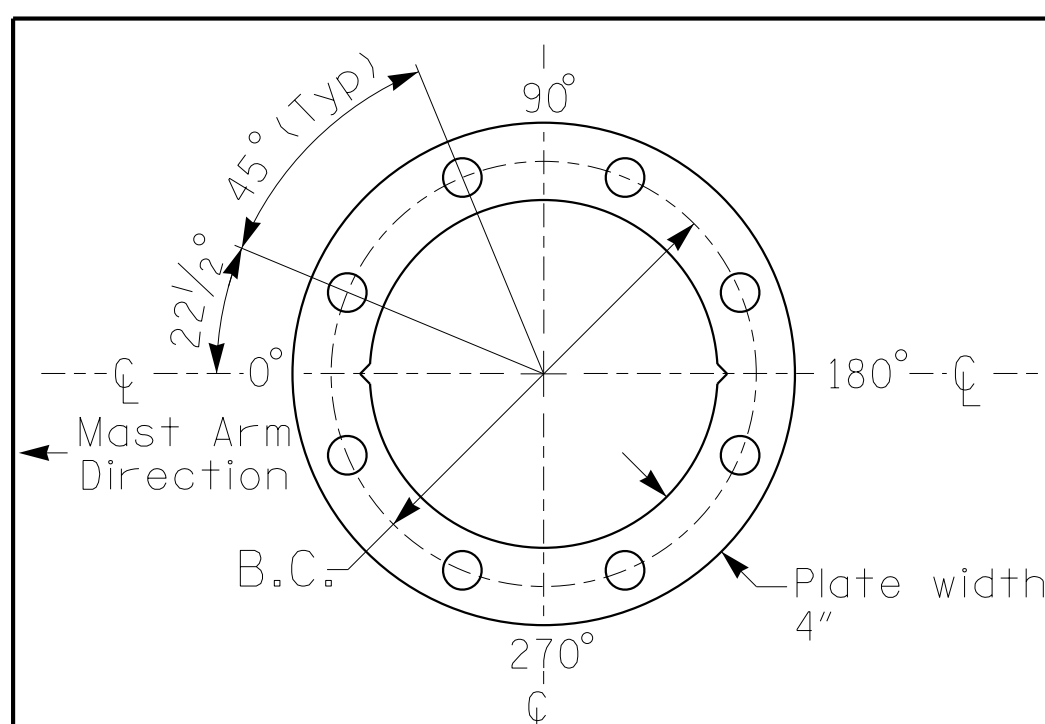


POLE RADIAL ORIENTATION



8 BOLT BASE PLATE DETAIL

See Note 5



BASE PLATE TEMPLATE & ANCHOR BOLT LOCK PLATE DETAIL For 8 Bolt Base Plate

NCDOT Wind Zone 4 (120 mph)

	SR 1320 (Airport Boulevard NW) at Chelsea Drive NW	SEAL
	Division 4 Wilson County Wilson PLAN DATE: August 2023 REVIEWED BY: ZML PREPARED BY: KGP, Jr. REVIEWED BY:	
SCALE 0 N/A N/A	REVISIONS INIT. DATE	DATE 10/27/2023 DATE DATE DATE

DESIGN REFERENCE MATERIAL

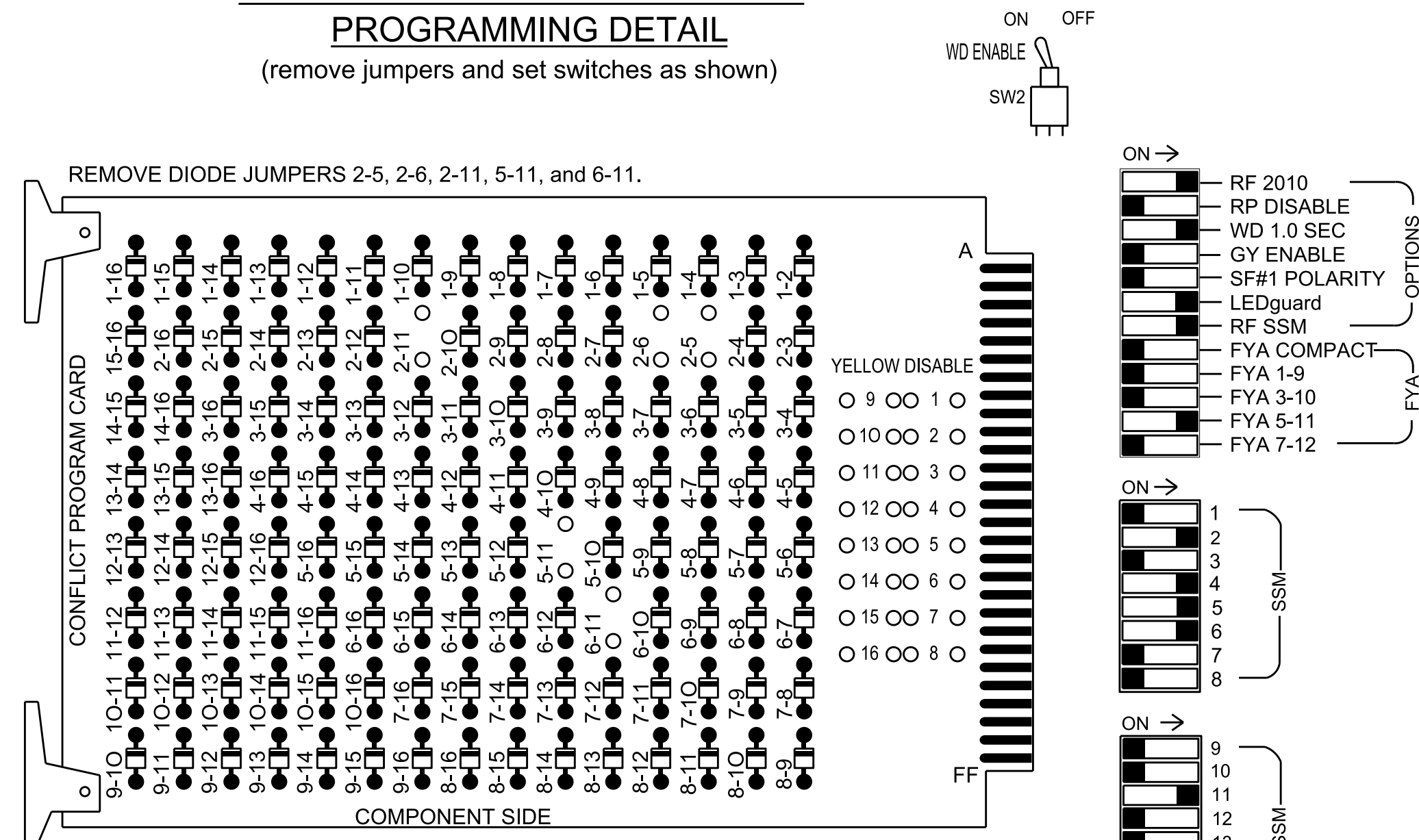
1. Design the traffic signal structure and foundation in accordance with:
- The 1st Edition 2015 AASHTO LRFD "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
 - The 2024 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
 - The 2024 NCDOT Roadway Standard Drawings.
 - The traffic signal project plans and special provisions.
 - The NCDOT "Metal Pole Standards" located at the following NCDOT website: <https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx>

DESIGN REQUIREMENTS

2. Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
3. Design all signal supports using force ratios that do not exceed 0.9.
4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements. This requires staggering the connections. Use elevation data for each arm to determine appropriate arm connection points.
6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
7. The mast arm attachment height (H1) shown is based on the following design assumptions:
- Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
 - Signal heads are rigidly mounted and vertically centered on the mast arm.
 - The roadway clearance height for design is as shown in the elevation views.
 - The top of the pole base plate is 0.75 feet above the ground elevation.
 - Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or
 - H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
9. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
10. The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
11. The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.

16 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
3. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
4. The cabinet and controller are part of the D04-17 Wilson System.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....18 With Aux. Output File
 Load Switches Used.....S2, S4, S5, S6, S12
 Phases Used.....2, 4, 5, 6
 Overlap "1".....NOT USED
 Overlap "2".....NOT USED
 Overlap "3".....NOT USED
 Overlap "4".....NOT USED

*See overlap programming detail on this sheet.

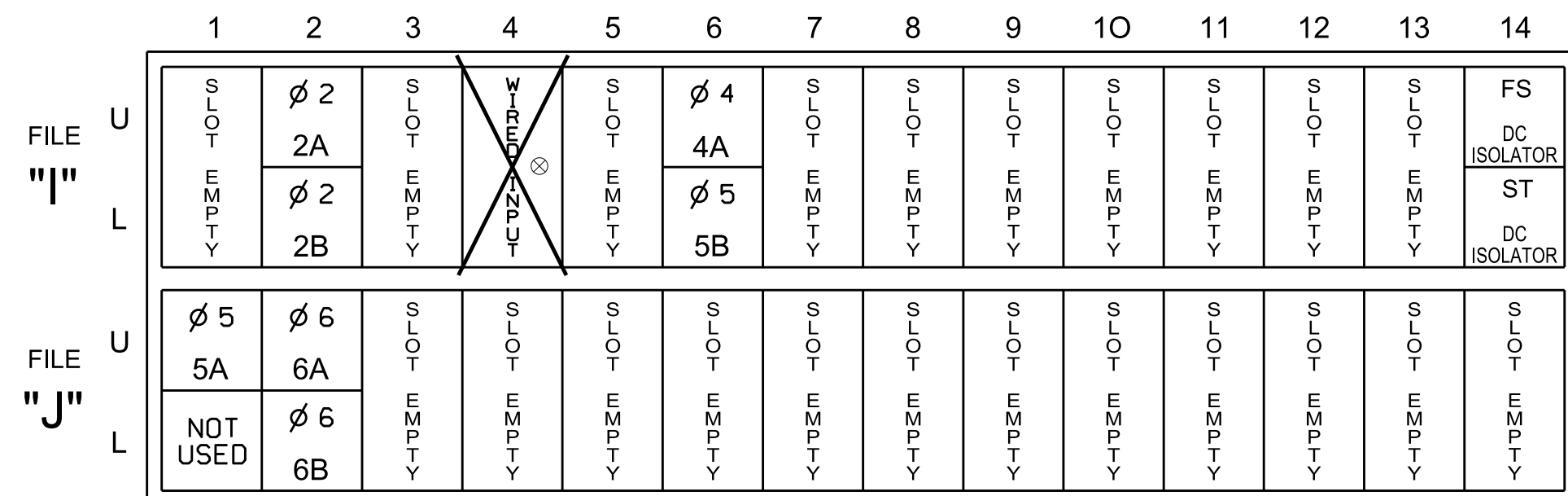
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
GMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	42	51	61,62	NU	NU	NU	NU	NU	NU	51	NU	NU
RED		128			101		*		134									
YELLOW		129			102				135									
GREEN		130			103				136									
RED ARROW																		A114
YELLOW ARROW							132											A115
FLASHING YELLOW ARROW																		A116
GREEN ARROW							133	133										

*See pictorial of head wiring in detail this sheet.
 *Denotes install load resistor. See load resistor installation detail this sheet.
 NU = Not Used

INPUT FILE POSITION LAYOUT

(front view)



EX. : 1A, 2A, ETC. = LOOP NO.'S

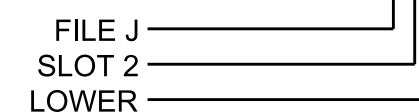
FS = FLASH SENSE
 ST = STOP TIME

NOTE: REMOVE EXISTING JUMPER ASSOCIATED WITH LOOP 5A FROM REAR OF INPUT FILE.

INPUT FILE CONNECTION & PROGRAMMING CHART

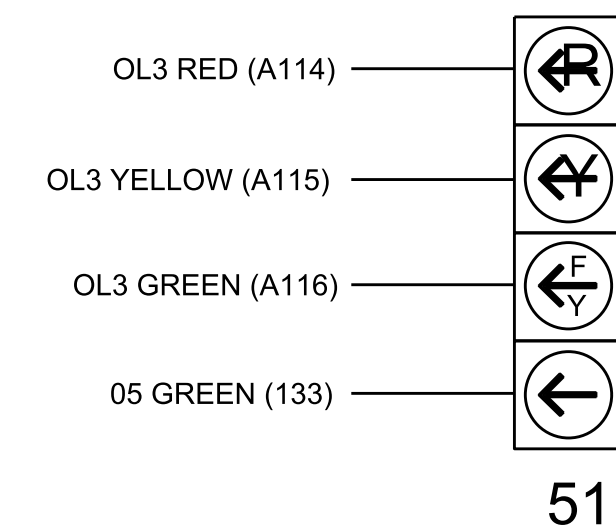
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A	TB2-5,6	I2U	39	1	2	2			X	X	X	
2B	TB2-7,8	I2L	43	5	3	2			X	X	X	
4A	TB4-9,10	I6U	41	3	8	4	3		X		X	
5A	TB3-1,2	J1U	55	17	15	5	15		X		X	
5B	TB4-11,12	I6L	45	7	9	5	15		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



OVERLAP PROGRAMMING

Front Panel
 Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
 Home >Controller >Overlap Configuration >Overlaps

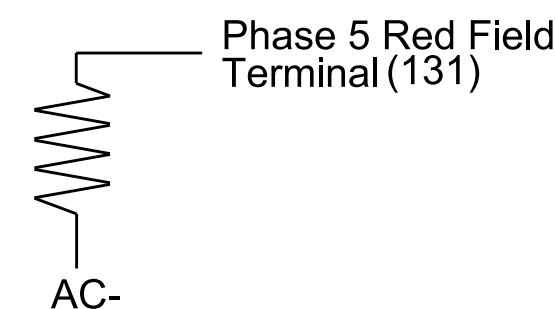
Overlap Plan 1

Overlap	1	2	3	4
Type	Off	Off	FYA 4 - Section	Off
Included Phases	-	-	6	-
Modifier Phases	-	-	5	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

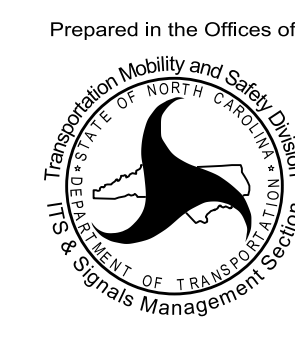
ACCEPTABLE VALUES	
Value (ohms)	Wattage
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0462
 DESIGNED: August 2023
 SEALED: 9/22/2023
 REVISED:

Electrical Detail

Electrical and Programming Details For:



750 N. Greenfield Pkwy, Garner, NC 27529

SR 1320 (Airport Boulevard) at Airport Drive

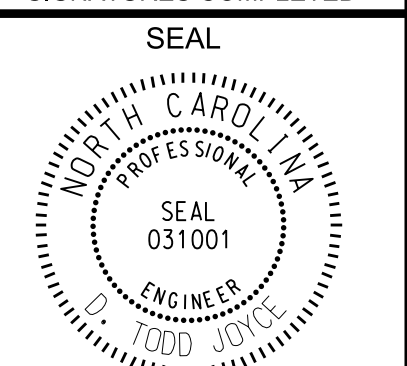
Division 4 Wilson County Wilson

Prepared by: D.J. Craddock REVIEWED BY: DTJ

PLAN DATE: October 2023 REVIEWED BY: DTJ

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

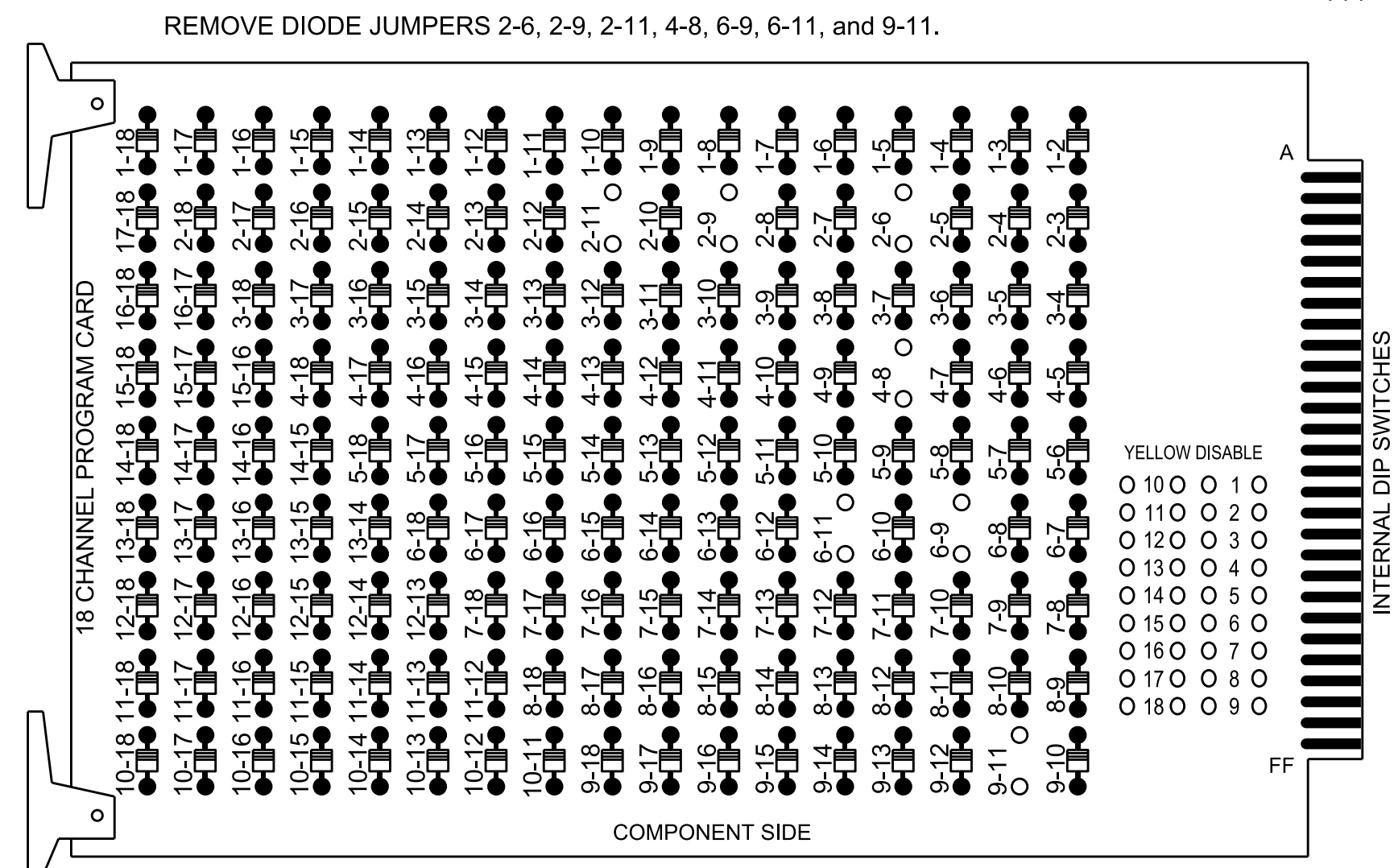


DocuSigned by: D. Todd Joyce 10/09/2023

SIG. INVENTORY NO. 04-0462

18 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that the Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plan.
2. Program phases 4 and 8 for Dual Entry.
3. Program controller to start up in phase 2 Green No Walk and 6 Green No Walk.
4. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
5. The cabinet and controller are part of the D04-17 Wilson System.

EQUIPMENT INFORMATION

Controller.....2070LX
 Cabinet.....332 w/ Aux
 Software.....Q-Free MAXTIME
 Cabinet Mount.....18 With Aux. Output File
 Load Switches Used.....S2, S5, S8, S11, AUX S1, AUX S4
 Phases Used.....2, 4, 6, 8
 Overlap "1".....*
 Overlap "2".....NOT USED
 Overlap "3".....*
 Overlap "4".....NOT USED

*See overlap programming detail on this sheet.

SIGNAL HEAD HOOK-UP CHART

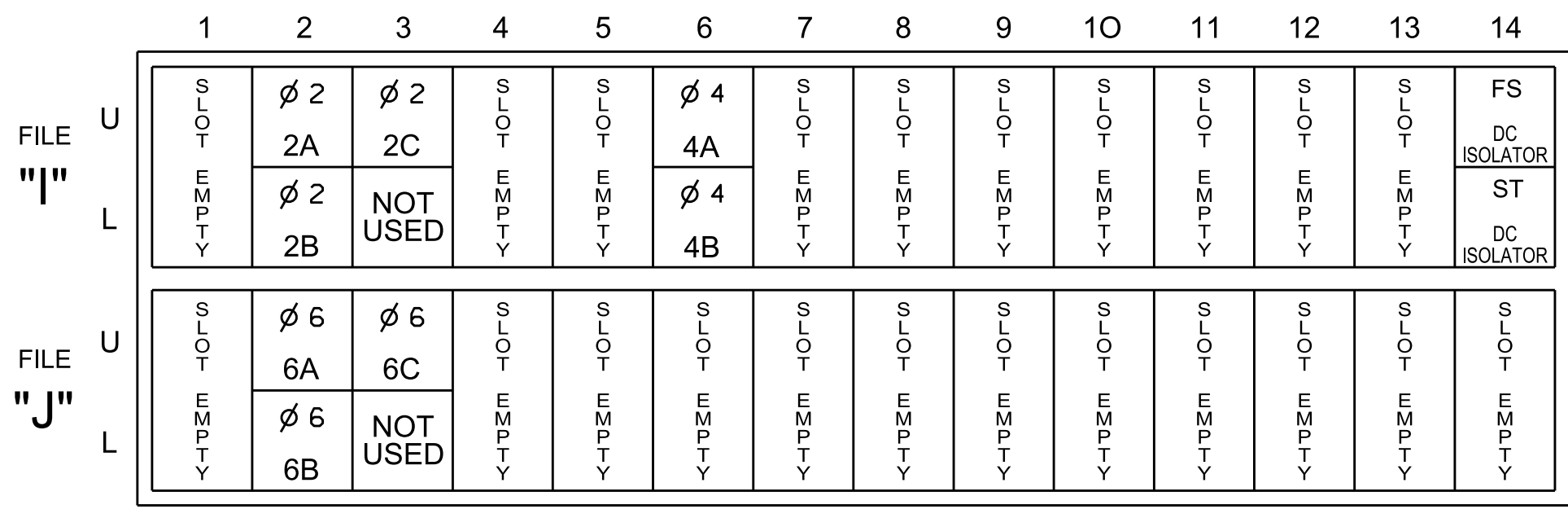
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OL1	OL2	SPARE	OL3	OL4	SPARE
SIGNAL HEAD NO.	NU	22,23	NU	NU	41,42	NU	NU	62,63	NU	NU	81,82	NU	61	NU	NU	21	NU	NU
RED		128			101			134			107							
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW																		

*See pictorial of head wiring in detail this sheet.

NU = Not Used

INPUT FILE POSITION LAYOUT

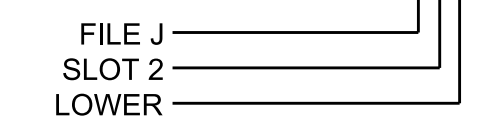
(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

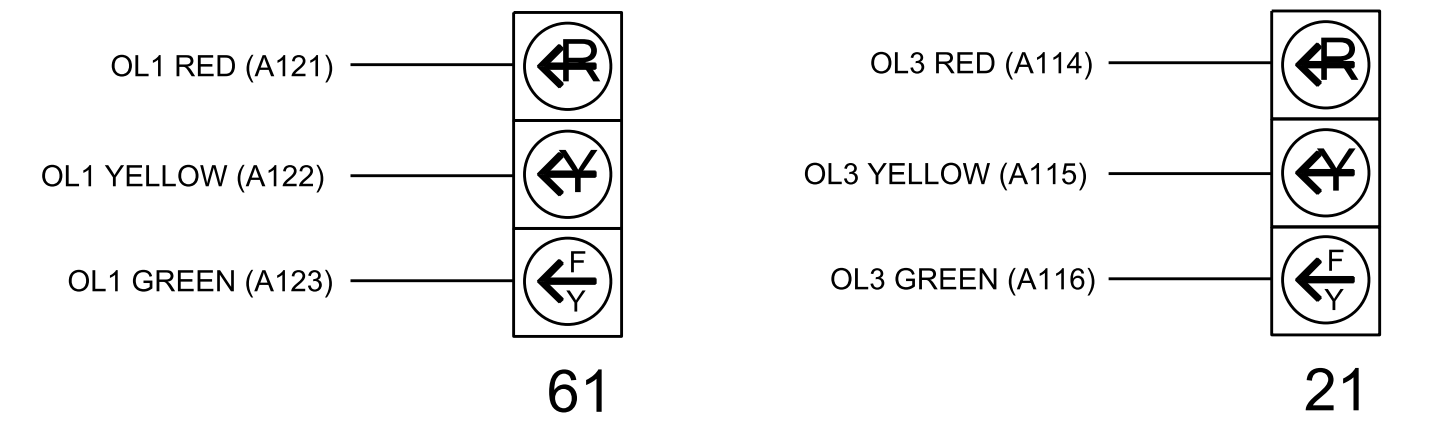
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT POINT	DETECTOR NO.	CALL PHASE	DELAY TIME	EXTEND TIME	EXTEND	ADDED INITIAL	CALL	DELAY DURING GREEN
2A	TB2-5,6	I2U	39	1	2	2			X	X	X	
2B	TB2-7,8	I2L	43	5	3	2			X	X	X	
2C	TB2-9,10	I3U	63	29	4	2	3		X		X	X
4A	TB4-9,10	I6U	41	3	8	4	3		X		X	
4B	TB4-11,12	I6L	45	7	9	4	10		X		X	
6A	TB3-5,6	J2U	40	2	16	6			X	X	X	
6B	TB3-7,8	J2L	44	6	17	6			X	X	X	
6C	TB3-9,10	J3U	64	30	18	6	3		X		X	X

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



OVERLAP PROGRAMMING

Front Panel
Main Menu >Controller >Overlap >Overlap Parameters/Overlap Timings

Web Interface
Home >Controller >Overlap Configuration >Overlaps

Overlap Plan 1

Overlap	1	2	3	4
Type	FYA 4 - Section	Off	FYA 4 - Section	Off
Included Phases	2	-	6	-
Modifier Phases	-	-	-	-
Trail Green	0	0	0	0
Trail Yellow	0.0	0.0	0.0	0.0
Trail Red	0.0	0.0	0.0	0.0

SPECIAL DETECTOR NOTE

Install a multizone microwave detection system for vehicle detection on Loop 8A. Perform installation according to manufacturer's directions and NCDOT engineer -approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1458
 DESIGNED: August 2023
 SEALED: 9/22/2023
 REVISED:

Electrical Detail

Electrical and Programming Details For:

Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1320 (Airport Boulevard NW) at SR 1316 (Jetstream Drive NW)

Division 4 Wilson County Wilson

PLAN DATE: October 2023 REVIEWED BY: DTJ

PREPARED BY: D.J. Craddock REVIEWED BY:

REVISIONS	INIT.	DATE

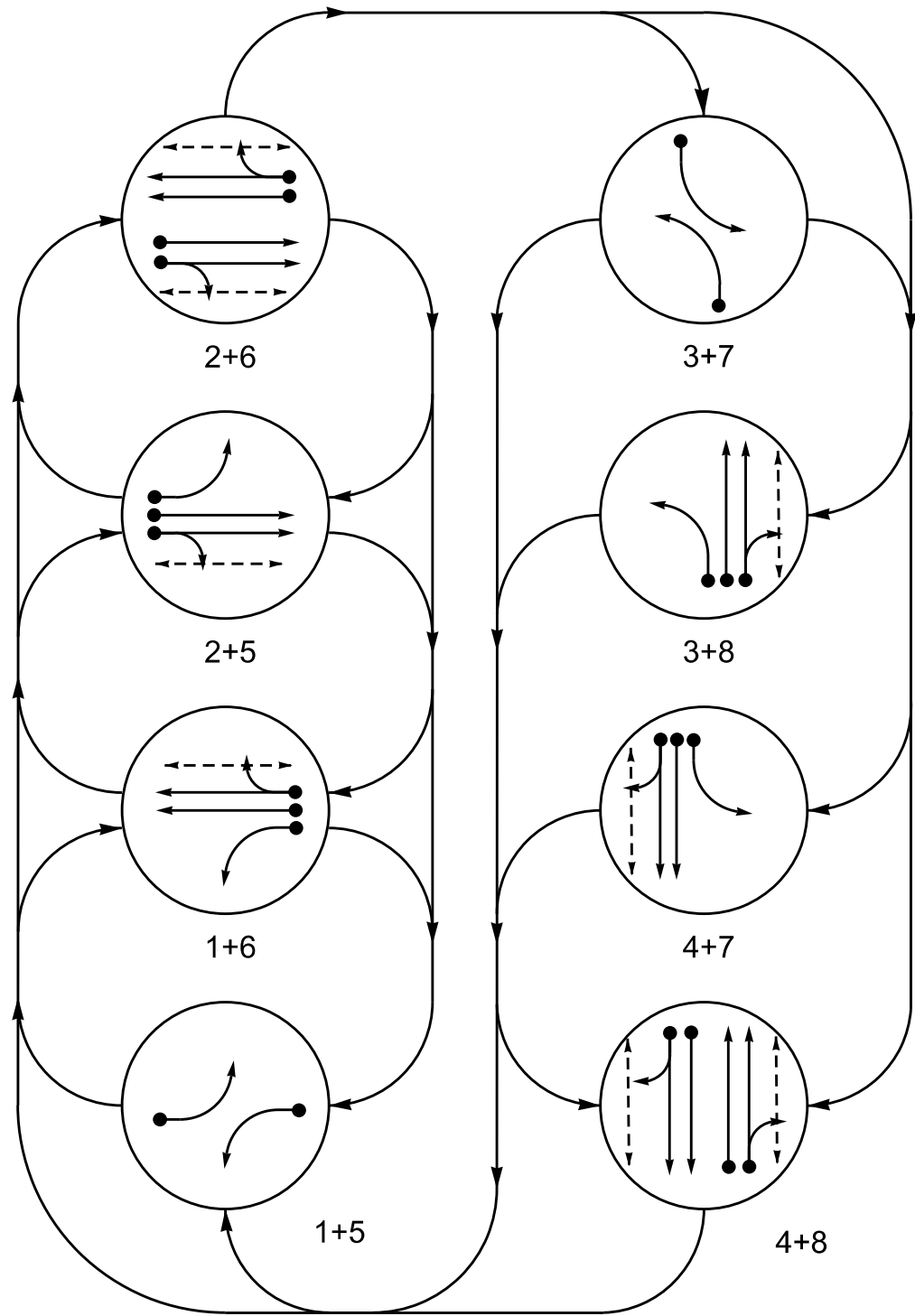
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

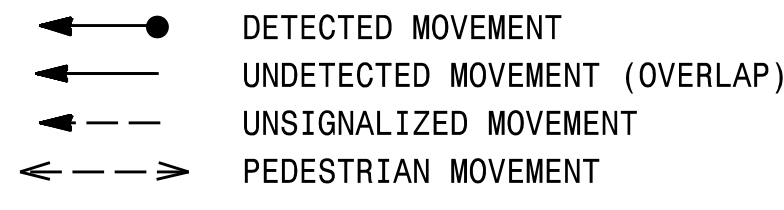
DocuSign by: D. Todd Joyce 10/09/2023

SIG. INVENTORY NO. 04-1458

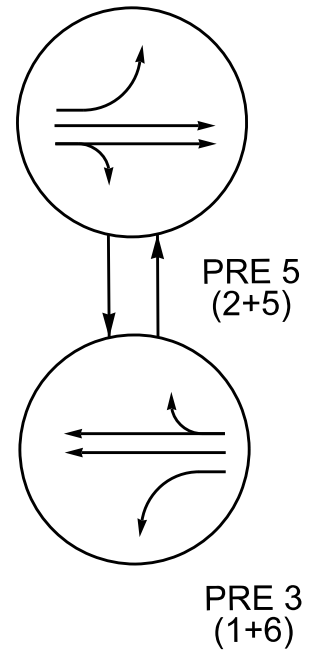
PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

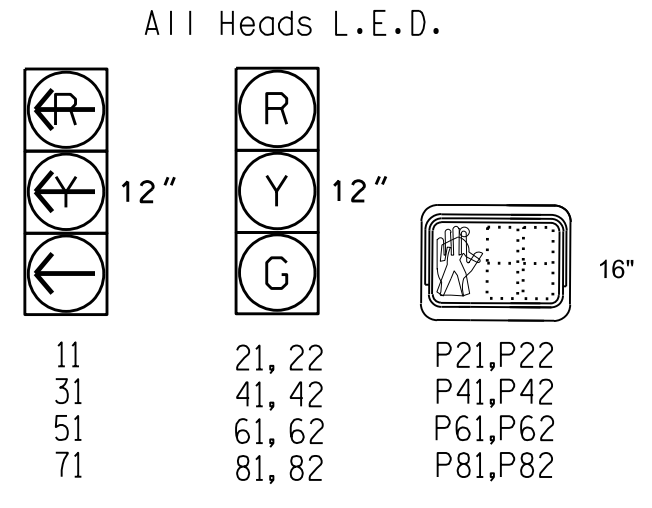


EV PREEMPT PHASES (Medium Priority)



SIGNAL FACE	PHASE											
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8	P1	P2	P3	P4
11	←	←	←	←	←	←	←	←	←	←	←	←
21, 22	R	R	G	G	R	R	R	R	R	G	R	R
31	←	←	←	←	←	←	←	←	←	←	←	←
41, 42	R	R	R	R	R	R	G	G	R	R	R	R
51	←	←	←	←	←	←	←	←	←	←	←	←
61, 62	R	G	R	G	R	R	R	R	R	G	R	R
71	←	←	←	←	←	←	←	←	←	←	←	←
81, 82	R	R	R	R	R	G	R	G	R	R	R	R
P21,P22	DW	DW	W	W	DW	DW	DW	DW	DW	DW	DRK	DRK
P41,P42	DW	DW	DW	DW	DW	DW	W	W	DW	DW	DRK	DRK
P61,P62	DW	W	DW	W	DW	DW	DW	DW	DW	DW	DRK	DRK
P81,P82	DW	DW	DW	DW	W	DW	W	DW	DW	DW	DRK	DRK

SIGNAL FACE I.D.



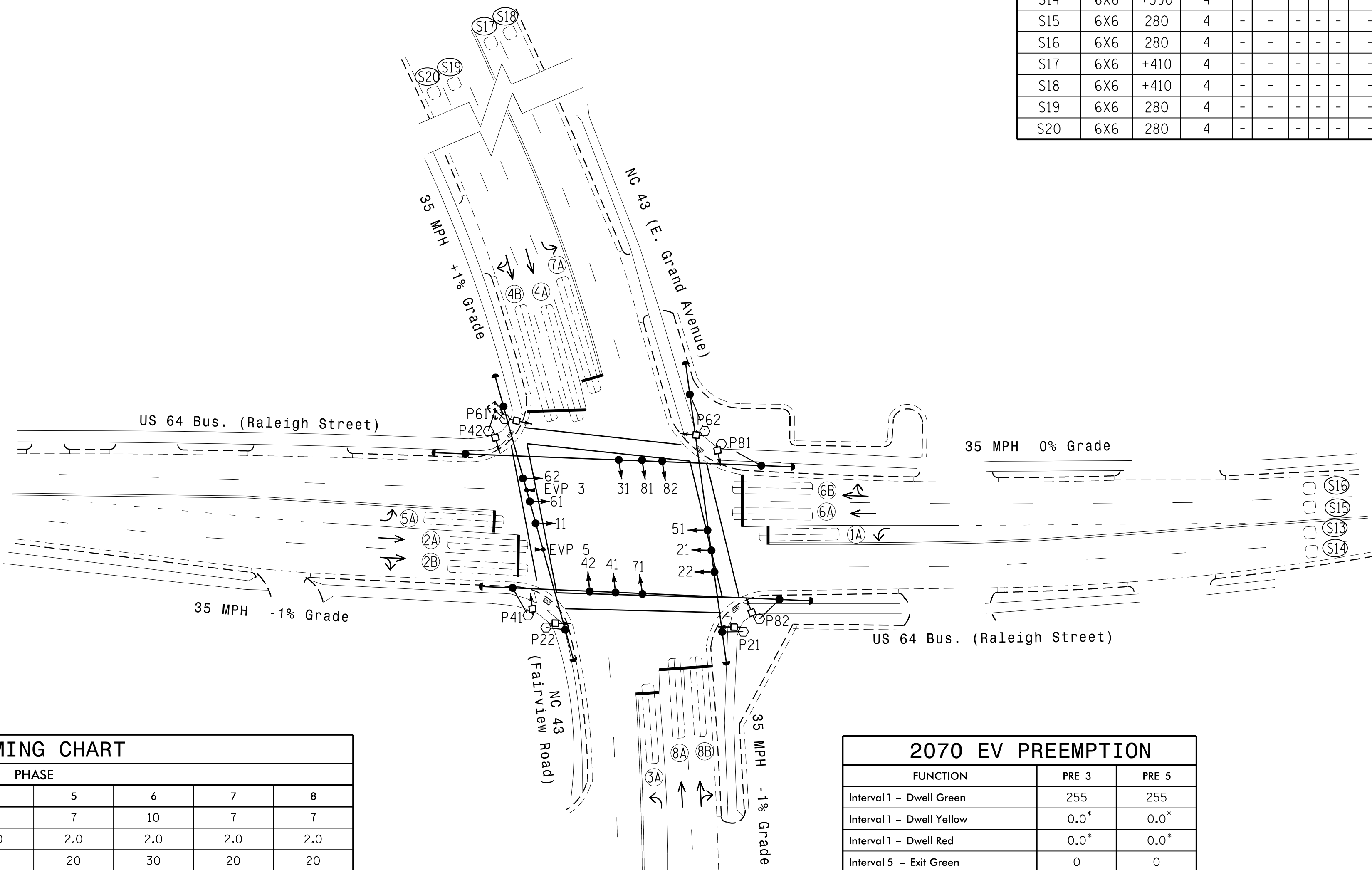
OASIS 2070L LOOP & DETECTOR INSTALLATION CHART

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING				SYSTEM LOOP	NEW CARD
					PHASE	CALLING	EXTENSION	FULL TIME DELAY		
1A	6X40	+5	2-4-2	-	1	Y	Y	-	3	-
2A	6X40	+5	2-4-2	-	2	Y	Y	-	-	-
2B	6X40	+5	2-4-2	-	2	Y	Y	-	-	-
3A	6X40	+5	2-4-2	-	3	Y	Y	-	3	-
4A	6X60	+5	2-4-2	-	4	Y	Y	-	-	-
4B	6X60	+5	2-4-2	-	4	Y	Y	-	10	-
5A	6X40	+5	2-4-2	-	5	Y	Y	-	3	-
6A	6X40	+5	2-4-2	-	6	Y	Y	-	-	-
6B	6X40	+5	2-4-2	-	6	Y	Y	-	-	-
7A	6X60	+5	2-4-2	-	7	Y	Y	-	3	-
8A	6X40	+5	2-4-2	-	8	Y	Y	-	-	-
8B	6X40	+5	2-4-2	-	8	Y	Y	-	10	-
S13	6X6	+390	4	-	-	-	-	-	-	Y
S14	6X6	+390	4	-	-	-	-	-	-	Y
S15	6X6	280	4	-	-	-	-	-	-	Y
S16	6X6	280	4	-	-	-	-	-	-	Y
S17	6X6	+410	4	-	-	-	-	-	-	Y
S18	6X6	+410	4	-	-	-	-	-	-	Y
S19	6X6	280	4	-	-	-	-	-	-	Y
S20	6X6	280	4	-	-	-	-	-	-	Y

8 Phase W/EV Preempt Fully Actuated Rocky Mount City System

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2024 and "Standard Specifications for Roads and Structures" dated January 2024.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Phase 3 and/or phase 7 may be lagged.
- Set all detector units to presence mode.
- Pavement markings are existing.
- This intersection features an optical preemption system. Shown locations of optical detectors are conceptual only.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- System data: Zone 7 Controller Asset # 0165.
- Program the controller to allow an Advanced Walk movement before phases 2,4,6, and 8 vehicle phases.



OASIS 2070L TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1*	7	10	7	7	7	10	7	7
Extension 1*	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Max Green 1*	20	30	20	20	20	30	20	20
Yellow Clearance	3.0	3.9	3.0	3.9	3.0	3.9	3.0	3.9
Red Clearance	2.8	2.1	3.4	2.2	2.8	2.1	3.3	2.2
Walk 1*	-	14	-	14	-	14	-	13
Don't Walk 1	-	23	-	20	-	23	-	19
Advance Walk 1	-	7	-	7	-	7	-	6
Seconds Per Actuation*	-	-	-	-	-	-	-	-
Max Variable Initial*	-	-	-	-	-	-	-	-
Time Before Reduction*	-	-	-	-	-	-	-	-
Time To Reduce*	-	-	-	-	-	-	-	-
Minimum Gap	-	-	-	-	-	-	-	-
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	-	-	-	-	-	-	-
Dual Entry	-	-	-	-	-	-	-	-
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

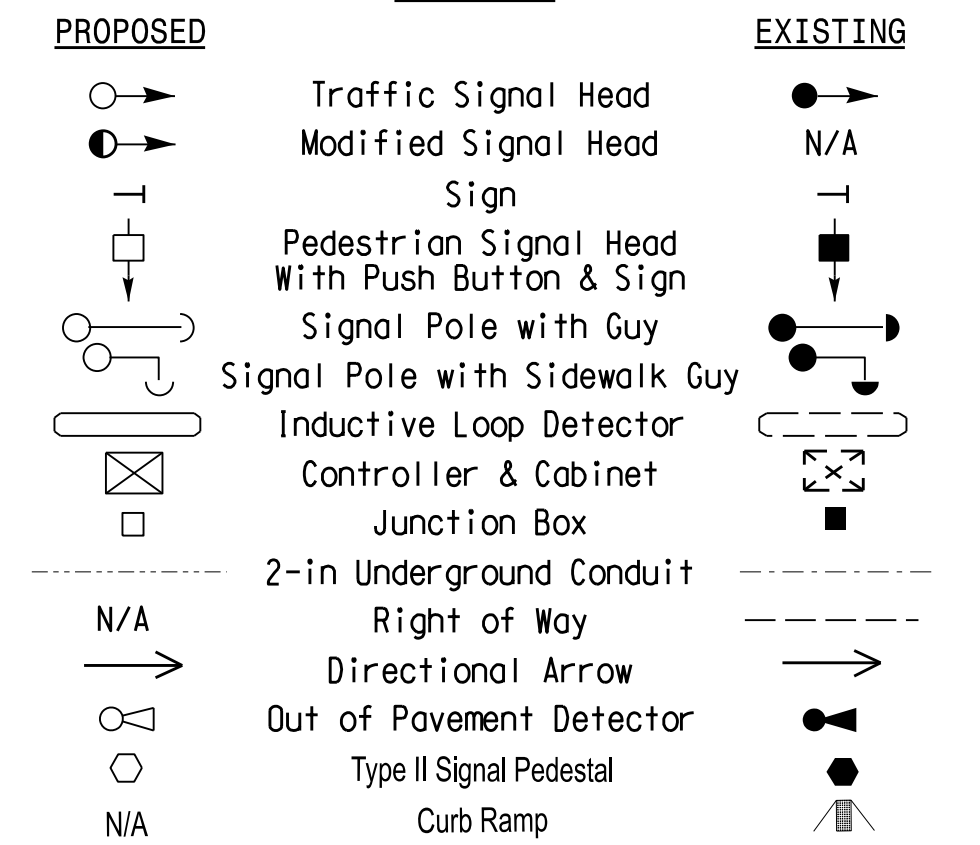
* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

2070 EV PREEMPTION

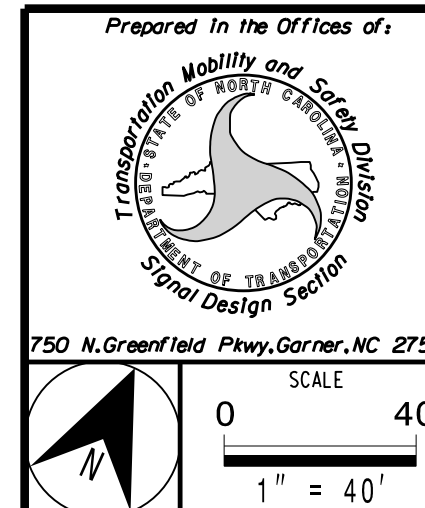
FUNCTION	PRE 3	PRE 5
Interval 1 - Dwell Green	255	255
Interval 1 - Dwell Yellow	0.0*	0.0*
Interval 1 - Dwell Red	0.0*	0.0*
Interval 5 - Exit Green	0	0
Interval 5 - Yellow	0.0	0.0
Interval 5 - Red	0.0	0.0
Priority	Medium	Medium
Delay Time	0.0	0.0
Min Green Before Pre	1	1
Ped Clear Before Pre	0	0
Yellow Clear Before Pre	0.0*	0.0*
Red Clear Before Pre	0.0*	0.0*
Dwell Min Time	10	10
Dwell Max Time	2	2
Enable Backup Protection	N	N
Ped Clear Through Yellow	Y	Y
Preempt Extend**	5	5
Omit Overlaps	-	-

* Time defaults to time used for phase during normal operation
** Program Timing on Optical Detection Unit

LEGEND



Signal Upgrade Corr. File No. 04-23-71183



US 64 Bus. (Raleigh Street) at NC 43 (Fairview Road) / (E. Grand Avenue)

Division 4 Edgecombe County Rocky Mount

PLAN DATE: April 2024 REVIEWED BY: BMH

PREPARED BY: Jeff Spence REVIEWED BY:

REVISIONS: INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

NORTH CAROLINA PROFESSIONAL ENGINEER

BAILEY M. HARRIER

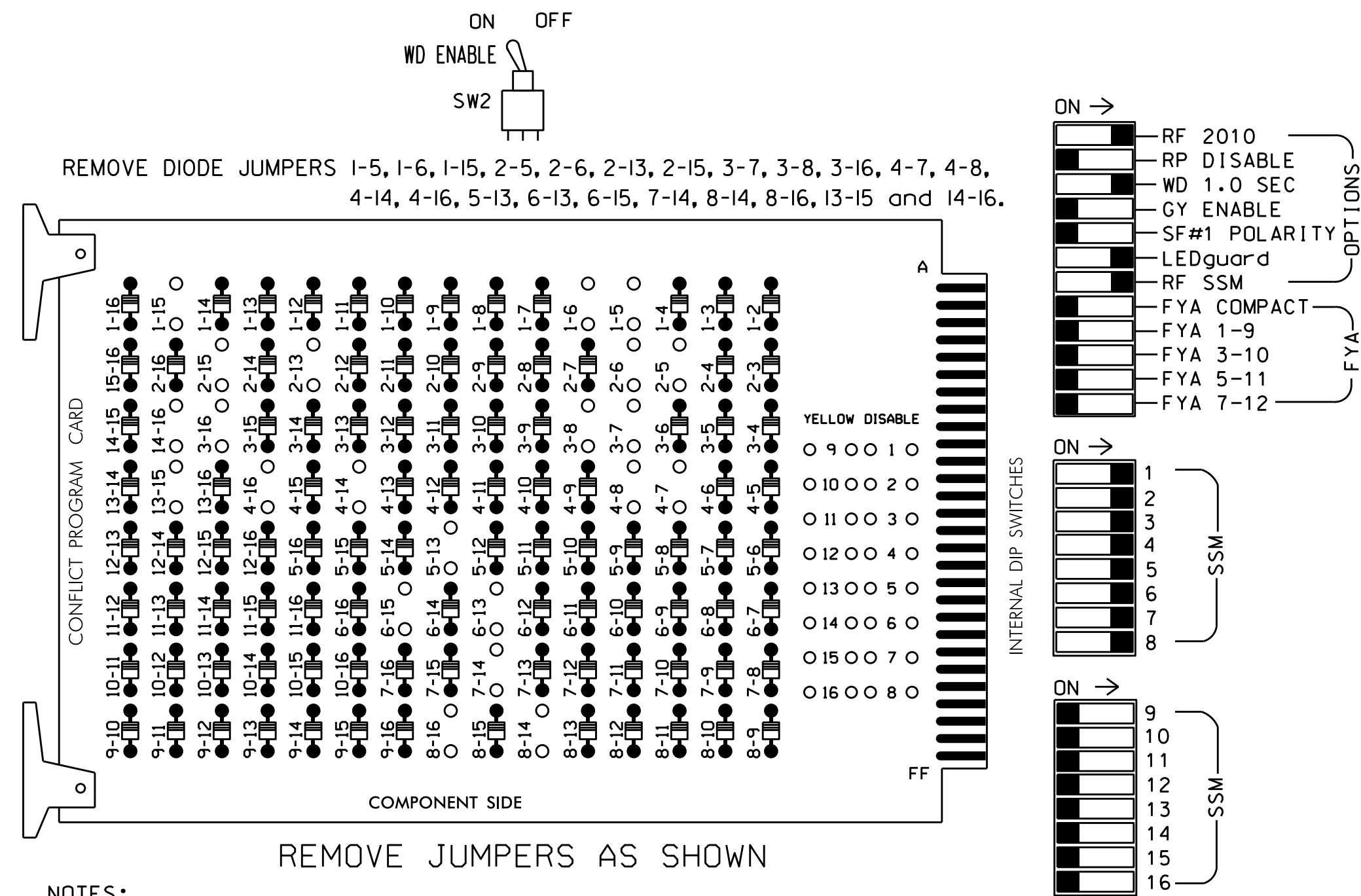
05/22/2024

SIG. INVENTORY NO. 04-0165

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16 CHANNEL CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE DIODE JUMPERS 1-5, 1-6, 1-15, 2-5, 2-6, 2-13, 2-15, 3-7, 3-8, 3-16, 4-7, 4-8, 4-14, 4-16, 5-13, 6-13, 6-15, 7-14, 8-14, 8-16, 13-15 and 14-16.

REMOVE JUMPERS AS SHOWN

- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 9,10, 11,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 as First Phases.
- Remove Yellow Flash programming and remove phases 2 and 6 for Startup in Green.
- The cabinet and controller are part of the Rocky Mount City System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S1,S2,S2P,S3,S4,S4P,S5,S6,S6P,S7,S8,S8P
 PHASES USED.....1,2,2 PED,3,4,4 PED,5,6,6 PED,7,8,8 PED
 OVERLAPS.....NONE

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	11	21,22	P21, P22	31	41,42	P41, P42	51	61,62	P61, P62	71	81,82	P81, P82
RED		128			101			134			107	
YELLOW		129			102			135			108	
GREEN		130			103			136			109	
RED ARROW	125			116			131			122		
YELLOW ARROW	126			117			132			123		
GREEN ARROW	127			118			133			124		
			113			104			119			110
			115			106			121			112

NU = Not Used

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14	
U	∅ 1 1A	∅ 2 2A	∅ 3 3A	∅ 4 4A	SYS. DET. S13	SYS. DET. S14	SYS. DET. S15	SYS. DET. S16	SYS. DET. S17	SYS. DET. S18	SYS. DET. S19	SYS. DET. S20	∅ 2 PED DC ISOLATOR	∅ 6 PED DC ISOLATOR	FS DC ISOLATOR
L	NOT USED	∅ 2 2B	NOT USED	∅ 4 4B	NOT USED	∅ 8 8B	NOT USED	∅ 8 8B	NOT USED	NOT USED	NOT USED	NOT USED	∅ 4 PED DC ISOLATOR	∅ 8 PED DC ISOLATOR	ST DC ISOLATOR
U	∅ 5 5A	∅ 6 6A	∅ 7 7A	∅ 8 8A	SYS. DET. S17	SYS. DET. S18	SYS. DET. S19	SYS. DET. S20	PRE3 * Optical 2-Card	PRE4	PRE5	PRE6	PRE7	PRE8	PRE9
L	NOT USED	∅ 6 6B	NOT USED	∅ 8 8B	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME
 EV PREEMPT = PRE 3 and PRE-5

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
* S13	TB6-1,2	I7U	65	27	34	SYS					
* S14	TB6-3,4	I7L	78	40	44	SYS					
* S15	TB6-9,10	I9U	60	22	11	SYS					
* S16	TB6-11,12	I9L	62	24	13	SYS					
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
7A	TB5-5,6	J5U	57	19	7	7	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10
* S17	TB7-1,2	J7U	66	28	38	SYS					
* S18	TB7-3,4	J7L	79	41	48	SYS					
* S19	TB7-9,10	J9U	59	21	15	SYS					
* S20	TB7-11,12	J9L	61	23	17	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

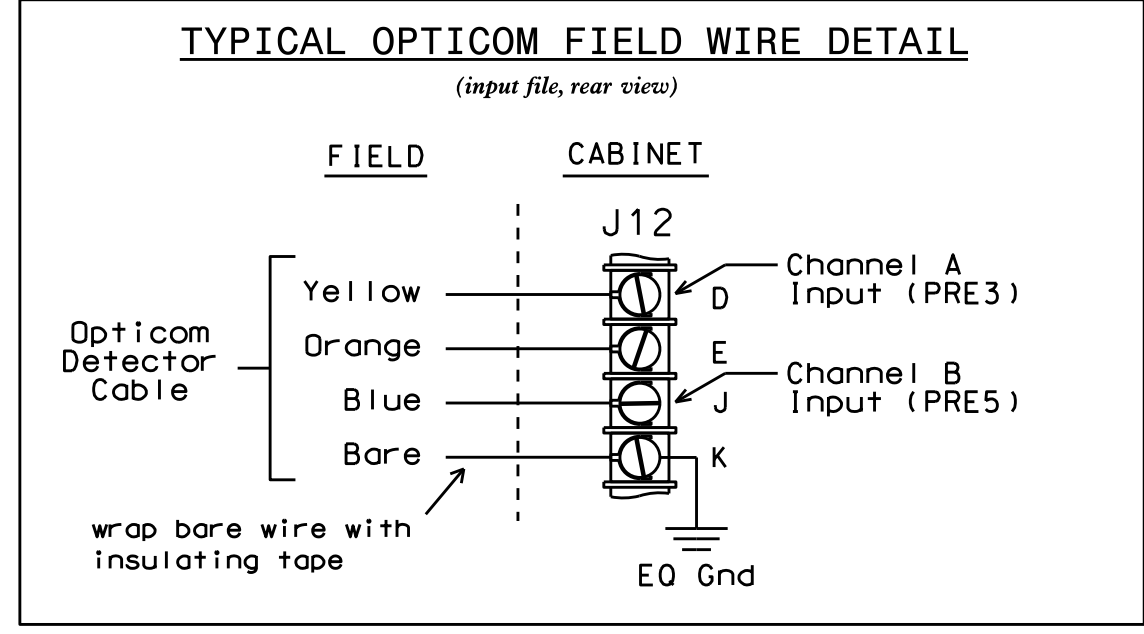
* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

INPUT FILE POSITION LEGEND: J2L
 FILE J
 SLOT 2
 LOWER

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0165
 DESIGNED: April 2024
 SEALED: 5/22/2024
 REVISED: N/A



Electrical Detail - Sheet 1 of 2

US 64 Bus. (Raleigh Street) at NC 43 (Fairview Road)/(E. Grand Avenue)

Division 4 Edgecombe County Rocky Mount

PLAN DATE: May 2024 REVIEWED BY:

PREPARED BY: Zarrar Zafar REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Corner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 031001
 D. Todd Joyce
 05/23/2024
 DATE
 SIG. INVENTORY NO. 04-0165

22-MAY-2024 16:28 S:\IT\SS\K15\S10\0165\Workgroups\519_Mon\Zafar\Plans\01\1\1\01_4\0010165\040165_sml.e16_2024rmds.dgn zzzcfar

**EMERGENCY VEHICLE PREEMPTION
PROGRAMMING DETAIL**

(program controller as shown below)

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions). Press the 'Next' key 2-times to advance to Preempt #3.

EVP 3 :



PREEMPTION #3	SETTINGS (NEXT:1-10)		
INTERVAL/TIMING	CLEAR/DWELL PHASES		
GRN YEL RED	12345678910111213141516		
1 255 0.0 0.0	X	X	
2 0 0.0 0.0			
3 0 0.0 0.0			
4 0 0.0 0.0			
5 0 0.0 0.0			

EXIT CALLS	
OPTIONS	
PRIORITY (Y/N TO SELECT)	MED
DELAY TIMER (0-255 SEC)	0.0
MIN GREEN BEFORE PRE (0= DEFAULT)...	1
PED CLEAR BEFORE PRE (0= DEFAULT)...	0
YELLOW CLEAR BEFORE PRE (0= DEFAULT)...	0.0*
RED CLEAR BEFORE PRE (0= DEFAULT)...	0.0*
DWELL MIN TIMER (0-255 SEC)	10
DWELL MAX TIMER (0=OFF,1-255MIN) ...	0
DWELL HOLD-OVER TIMER (0-255)	0
LATCH CALL?	N
LINK TO NEXT PREEMPT?	N
ENABLE BACKUP PROTECTION?	N
HOLD CLEAR 1 PHASES DURING DELAY? ..	N
FAST GREEN FLASH DWELL PHASES?	N
PED CLEARANCE THROUGH YELLOW?	Y
INHIBIT OVERLAP GREEN EXTENSION? ..	N
SERVICE DURING SOFTWARE FLASH?	N
REST IN RED DURING DWELL INTERVAL? ..	N
FLASH DWELL INTERVAL?	N
ALLOW PEDS IN DWELL INTERVAL?	N
RE-TIME DWELL INTERVAL?	N
OVERLAPS:	ABCDEFGHIJKLMNOP
DWELL INT FLASH YELLOW	
OMIT OVERLAPS:	

PRESS 'NEXT' TWICE

EVP 5 :

PREEMPTION #5	SETTINGS (NEXT:1-10)		
INTERVAL/TIMING	CLEAR/DWELL PHASES		
GRN YEL RED	12345678910111213141516		
1 255 0.0 0.0	X	X	
2 0 0.0 0.0			
3 0 0.0 0.0			
4 0 0.0 0.0			
5 0 0.0 0.0			

EXIT CALLS	
OPTIONS	
PRIORITY (Y/N TO SELECT)	MED
DELAY TIMER (0-255 SEC)	0
MIN GREEN BEFORE PRE (0= DEFAULT)...	1
PED CLEAR BEFORE PRE (0= DEFAULT)...	0
YELLOW CLEAR BEFORE PRE (0= DEFAULT)...	0.0*
RED CLEAR BEFORE PRE (0= DEFAULT)...	0.0*
DWELL MIN TIMER (0-255 SEC)	10
DWELL MAX TIMER (0=OFF,1-255MIN) ...	0
DWELL HOLD-OVER TIMER (0-255)	0
LATCH CALL?	N
LINK TO NEXT PREEMPT?	N
ENABLE BACKUP PROTECTION?	N
HOLD CLEAR 1 PHASES DURING DELAY? ..	N
FAST GREEN FLASH DWELL PHASES?	N
PED CLEARANCE THROUGH YELLOW?	Y
INHIBIT OVERLAP GREEN EXTENSION? ..	N
SERVICE DURING SOFTWARE FLASH?	N
REST IN RED DURING DWELL INTERVAL? ..	N
FLASH DWELL INTERVAL?	N
ALLOW PEDS IN DWELL INTERVAL?	N
RE-TIME DWELL INTERVAL?	N
OVERLAPS:	ABCDEFGHIJKLMNOP
DWELL INT FLASH YELLOW	
OMIT OVERLAPS:	

PROGRAMMING COMPLETE

* TIME DEFAULTS TO NORMAL PHASE TIMING.

Program extend time on optical detector units for 5.0 sec.

ADVANCED WALK NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phases 2, 4, 6 and 8 for 'Advanced Walk'. Make sure the Walk Advance Time shown on the Signal Design plans are programmed in the 'Phase Timing' menu.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-0165
DESIGNED: April 2024
SEALED: 5/22/2024
REVISED: N/A

Electrical Detail - Sheet 2 of 2

**DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED**

ELECTRICAL AND PROGRAMMING
DETAILS FOR:

Prepared in the Offices of:

750 N. Greenfield Pkwy, Corner, NC 27529

US 64 Bus. (Raleigh Street) at NC 43 (Fairview Road)/ (E. Grand Avenue)	
Division 4	Edgecombe County Rocky Mount
PLAN DATE: May 2024	REVIEWED BY:
PREPARED BY: Zarrar Zafar	REVIEWED BY:
REVISIONS	INIT. DATE

SEAL

Seal of Todd Joyce, Engineer, Seal 031001

Documented by: *Todd Joyce* 05/23/2024

DATE

SIG. INVENTORY NO. 04-0165